

High Speed Digital Design A Handbook Of Black Magic

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Focusing on a combination of digital and analog circuit theory, this comprehensive volume will help engineers who work with digital systems, shorten their product development cycles, and fix their latest high-speed design problems. Covers signal reflection, crosstalk, and noise problems that occur in high-speed digital machines (above 10 megahertz).

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Experience shows that for a high-speed digital design that uses low-impedance devices and high currents in the circuit, designers need to be concerned about mutual inductance. A mutual inductance can cause either a positive- or negative-edge crosstalk on another signal based on the direction of the loop present and the duration equal to the rise time of the aggressive signal.

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Written by three leading Intel engineers, High-Speed Digital System Design clarifies difficult and often neglected topics involving the effects of high frequencies on digital buses and presents a variety of proven techniques and application examples.

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Let ' s now consider what happens when a high speed edge is generated at the output of a digital device, and switches from a logic 0 (0V) to 1 (3.3V). For the purposes of this example, the edge has a rise time of 200ps and switches at a rate of 10 MHz. The first thing to note is that the 10 MHz switching rate is irrelevant.

Design focus: High speed digital design and termination ...

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This application report addresses high-speed signals, such as clock signals and their routing, and gives designers a review of the important coherences. With some simple rules, electromagnetic interference problems can be minimized without using complicated formulas and expensive simulation tools.

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High-Speed Layout Guidelines - Texas Instruments

This is Part 1 of a multi-part series. Please follow SIGMADESIGN on LinkedIn to read more! SIGMADESIGN is well-armed with the expertise to tackle any engineering problem, and high-speed digital electronic design is certainly no exception. If your product must work with large volumes of data (such as video, networking, or any component or peripheral of a personal computer or smartphone), competent high-speed digital design will be a critical component to its success.

High-Speed Digital Design (Part 1) | SIGMADESIGN

High-speed Digital Design: A Handbook of Black Magic. High-speed Digital Design. : Howard W. Johnson, Martin Graham. Prentice Hall, 1993 - Technology & Engineering - 447 pages. 1 Review. Focused on the field of knowledge lying between digital and analog circuit theory, this new text will help engineers working with digital systems shorten their product development cycles and help fix their latest design problems.

High-speed Digital Design: A Handbook of Black Magic ...

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High Speed Digital Design: Design of High Speed ...

Description. Practical High Speed Digital Design will teach engineers the techniques necessary to meet the challenges associated with high speed designs. In this class you will hear a lot of theory and principals, see some simulations, and measurements. Board designers and chip designers Designing that cutting edge ASIC is a great asset ,Being able to implement that design on a board is also important Having both of these capabilities differentiates you.

Practical High-Speed Digital Design | Udemy

Focusing on a combination of digital and analog circuit theory, this comprehensive volume will help engineers who work with digital systems, shorten their product development cycles, and fix their latest high-speed design problems.

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High Speed Digital Design discusses the major factors to consider in designing a high speed digital system and how design concepts affect the functionality of the system as a whole. It will help you understand why signals act so differently on a high speed digital system, identify the various problems that may occur in the design, and research solutions to minimize their impact and address their root causes.

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